

Embedded FPGA (eFPGA) Solutions Architect Manager – March 2021

RESPONSIBILITIES: Manage an elite team responsible for pre-sales and post sales technical engagements for Flex Logix's embedded FPGA solutions. This team will work with customers and internal teams to design EFLX eFPGA IP into Customers' SOCs. Build relationships with customer's technical teams, then provide perspective on technical issues and our products. Break down technical barriers that prohibit product adoption. Advise executive management on critical technical issues and make recommendation on resolution options. Understand the corporate business objectives and ensure technical priorities are aligned. Prioritize customer evaluations and technical support needs with Sales and R&D. Work with marketing, salesm software and engineering on application notes and articles.

Report to the VP of Sales and Marketing for IP. Work closely with business team on developing our design win/customer engagement strategy with feedback on the impact of technical barriers to business goals.

Manager solution architecture team:

EXPERIENCE AND SKILL REQUIRED

Understanding of FPGA architectures, software tools and ASIC design flows. Application knowledge of FPGA or eFPGA use in a variety of market applications including but not limited to wireless, 5G, networking, storage market applications.

- Build an elite technical team
- Set team objectives and coordinate/prioritize workloads
- Ensure continued skill development
- Develop/coach team members on career and technical knowledge
- Extensive training and/or experience working with RTL designs and programming eFPGA/FPGAs.
- Preferably familiar with BNN, CNN, DNN, LSTM, etc. neural networks
- Experience with programming embedded processors
- Experience with FPGA applications requiring extensive DSP use, e.g. FIR, FFT filters, etc. Experience working directly with customers supporting significant design wins
- Experience managing a technical team
- Familiarity with ASIC design flows
- Comfortable in hand-on lab environments
- Strong verbal and written communication skills, enthusiastic presentation style

The follow skills are preferred for this candidate:

- 5+ years of experience as an SoC, MCU or ASIC designer
- Knowledge of VHDL or Verilog

- Knowledge of FPGA software tools
- Experience dealing with physical design teams
- Understanding of the digital EDA design flow
- C, C++ experience

We have solutions architects/application engineers that are very experienced in the use of eFPGA – these people will train you on what you need to know regarding eFPGA.

Must be willing to travel one week per month. Some travel will be international for customer meetings and industry events where we make presentations or have booths to promote our products. Customers typically work with us over Webex but we often need to meet initially and periodically during evaluation and implementation.

Must be passionate about doing this job: wanting to change the world and work hard doing it.
Must be entrepreneurial in spirit and an innovative problem solver.

BS EE/CS or higher.

Must live in Silicon Valley or Austin, TX

Must have US citizenship or US permanent residency ("green card").