Hardware System Engineer – March 2021

Flex Logix has announced its' first AI/ML Inference Accelerator CoProcessor, InferX X1 based on our Reconfigurable Tensor Processor IP. We are gearing towards first customer ship of the InferX SoC and a family of X1 based products, and are looking for a hands-on Hardware System engineer to join our team to move our system board products from engineering phase to production, reporting to the System Engineering Director. As a System engineer, you will work on hardware systems projects to craft the solutions for current and future AI/ML inference products.

We recently raised $55M in Series D funding to accelerate our X1 sales ramp.

RESPONSIBILITIES

- Own, define and implement the Flexlogix's Inference adaptors (X1P1 & X1M), hardware system tasks, which include the system function specification, and verification at system level, as well as at circuit level.
- Provide design guidance to JDM partner on electrical, SI, mechanical and thermal designs based on the Product System Specification. Provide timely review feedbacks on schematics, board layouts, SIPI, Mechanical and thermal designs. Be the internal owner of all above issues.
- As a member of the core bring-up team, work with Architecture and SW team members on X1M bring-up and function enablement. Implement system FW functions, as well as needed system level stress codes and scripts.
- Working closely with Manufacturing Engineer, JDM partner, System QA engineer, to design and implement the HW and SW aspect of the Manufacturing Product Test Env Development
- As the key point of contact, is responsible for the chip and board reference design documentation, provide all needed design files to enable the OEM design teams, participate OEM vendor technical meetings, review and provide the final approval on OEM submitted designs. Be able to travel to OEM site to help the vendors bringing-up their products.

EXPERIENCE AND SKILL REQUIRED

BSEE/MSEE with at least 5+ years of relevant industry experience.

Must have hands-on experience on designing and releasing multi-generations of boards, and releasing them to production.

The engineer should have solid computer architecture knowledge. Be proficient on the PCIe, DDR, I2C protocols; Be proficient on SIPI design principles and challenges; Understand the mechanical and thermal design principles.

On the SW side, the engineer should be familiar with Linux system level programming. Fluent with Python, Expect, Perl and C and C++ programming languages.

Past design experience with schematics, board layout and debug experiences is a must.

The preferred engineer will be a self-learner, and is very organized, can perform the task with minimum guidance.

Must be passionate about being part of an aggressive, venture-backed startup team that is changing the way chips and boards are architecture, designed and programmed.

Must be an entrepreneurial, innovative problem solver and willing to work hard.

MUST live in Silicon Valley and have US citizenship or permanent residency ("green card") or an H1-B visa