

## Physical Design Engineer

Flex Logix is the leader in eFPGA with working silicon and customers in 40, 28/22, 16/12 and 14/12 nm nodes. We are starting now on 7/6nm EFLX eFPGA. Our eFPGA business did \$16M in revenue last year and is cash-flow positive with significant growth expected in 2021.

Flex Logix also has developed an industry-leading inference architecture with superior inference throughput/\$. Our InferX X1 chip and boards are sampling now and will be in production mid-year.

We recently completed a \$55M funding round to accelerate growth of our eFPGA and Inference businesses.

### **RESPONSIBILITIES**

Physical Design Engineer involved in digital and circuit design of EFLX (embedded FPGA) and nnMAX (inference accelerator) cores in 40nm, 28/22nm, 16/12nm, 14/12nm and 7/6nm. We have started on our first TSMC N7/N6 design.

- Responsible for all aspects of silicon design including:
  - o PnR (RTL to GDS) and timing closure for EFLX cores in different process nodes (40nm, 28nm, 16nm, 14nm, 7nm)
  - o optimization of custom digital cell library used for Look Up Tables, DSP and Interconnect switches for optimal performance, power and area (PPA)
  - o timing closure (RTL to GDS) for EFLX/NMAX cores
  - o Timing Verification for EFLX/NMAX cores
  - o EM and IR analysis of EFLX/NMAX cores
  - o DFT/ATPG pattern generation
  - o Validation (testing) of the EFLX/NMAX cores

### **EXPERIENCE AND SKILL REQUIRED**

BSEE/MSEE

Will consider a New College Graduate. Two or more years of relevant industry experience preferred.

Must be very smart and very motivated

Must have hands-on experience in Back-End Physical Design (RTL to GDS) PnR using tools such as Cadence Innovus or Synopsys ICC.

Preferred experience OR willing to quickly learn:

- Standard cell library development including LIB/LEF development
- Synthesis of the EFLX/NMAX cores

- Scripting language such as Perl, Python
- RTL design

Must be passionate about being part of an aggressive, venture-backed startup team that is changing chip architecture. Must be entrepreneurial, innovative problem solver and willing to work hard.

MUST live in Silicon Valley or Austin TX and have US citizenship or permanent residency (“green card”), or holding a current H1-B visa