

## **AI Inference SOC Architect**

Flex Logix develops industry-leading AI inference engines and we are the #1 provider of eFPGA solutions. Our InferX X1 is the industry's fastest and most-efficient AI edge inference accelerator that brings AI to high-volume applications, surpassing competitor's performance at 1/7th size and much lower price. InferX X1 is available as a chip, PCIe board and M.2 board. InferX X1 is working and running YOLOv3 today and starts production shipments this summer.. Our Inference Compiler is easy to use (we take in neural network models in TensorFlowLite and ONNX) and our APIs for Infer X1 allow rapid integration by the customer. Our architecture is covered by dozens of patents and is highly differentiated giving us a sustainable competitive advantage.

We recently completed a \$55M funding round.

### **RESPONSIBILITIES**

The Inference SOC architect is a critical leadership position for our inference products. This individual will be responsible for defining future inference products as well as ensuring that the products are correctly built and validated. The architect will work closely with the software teams as well as the sales and marketing leaders to define innovative and winning inference solutions. The architect will ensure that logic and physical implementations are completed with the desired quality and costs. Finally, the SOC architect will work with IP providers and partners to select the best IP as well as ensure that they will deliver desired functionality.

#### **Primary Responsibilities:**

- Architect SOC solutions catered to inference products that include eFPGA
- Ensure that product definition meets performance needs (PPA)
- Ensure proper implementation and quality of the architecture
- Interface with IP providers for IP selection as well as feature definition
- Key technical interface to customers
- Collaborate with sales and marketing for defining best in class products
- Clearly document architecture

### **EXPERIENCE AND SKILL REQUIRED**

- Extensive experience defining SOC's that have reached production
- Experience estimating power of an SOC
- Experience estimating performance of an SOC
- Experience estimating size of an SOC
- Recent experience with selecting and working with major IP providers in the industry
- Recent hands-on experience implementing logic or integrating major IP in an SOC
- Recent hands-on experience debugging an SOC (both in simulation and post-Si)
- Excellent communication skills
- BS/MS EE/CS with 10 or more years of relevant industry experience
- Must be innovative and be able to cater solutions to particular designs

Preferred experience OR willing to quickly learn:

- Understanding of computer architectures for artificial intelligence
- Experience architecting designs for FPGAs
- Understanding of FPGA architectures
- Running logic synthesis and static timing tools
- Using SystemC or similar architecture modeling tools

Must be passionate about being part of an aggressive, venture-backed startup team that is changing chip architecture. Must be entrepreneurial, innovative problem solver and willing to work hard.

Must live in Silicon Valley or Austin area and have US citizenship or permanent residency (“green card”), or holding a current H1-B visa