EFLX eFPGA Sr. Software Engineer (Graphical User Interface)

Flex Logix is developing industry-leading AI inference engines and we are the #1 provider of eFPGA solutions. Our InferX X1 is the industry's fastest and most-efficient AI edge inference accelerator that will bring AI to the masses in high-volume applications, surpassing competitor's performance at 1/7th size and much lower price. InferX X1 is available as a chip, PCIe board and M.2 board. InferX X1 is working and running YOLOv3 today; we launched it at the Linley 2020 Fall Processor Conference. Our Inference Compiler is easy to use and our APIs for Infer X1 allow rapid integration by the customer. Production shipment and compiler availability commences mid-2021. Our architecture is covered by dozens of patents and is highly differentiated giving us a sustainable competitive advantage.

We recently completed a $55M funding round.

Flex Logix is a rapidly expanding, high growth company and is seeking a **Sr. Software Engineer**. Our products are very cost effective but very high performance, so both the hardware and software need to work together seamlessly and efficiently. The right candidate needs to be able to **design and create a new graphical user interface for EFLX eFPGA compiler.**

Responsibilities:

- Design and implement an eFPGA layout/floorplan viewer.
- Design and implement other features as required.
- Interface the layout/floorplan viewer to the Tcl Programing language "C" API.
- Create interfaces/dialogs which will execute Tcl commands from the GUI.
- Design and implement logging framework.

Qualifications:

BS/MS Computer Science, or equivalent, with 5+ years of relevant industry experience.

Must have hands-on experience with C++ and C programming language.

Must have hands-on experience using Qt graphical library or has developed an EDA tool's graphical user interface using an equivalent graphical library.

 Preferred experience OR willing to quickly learn:

- Tcl programming language.
- GUI development experience using Qt
- Software development experience on an FPGA/EDA tool.

We are looking for a passionate individual who would like to change the world. They must be entrepreneurial in spirit, an innovative problem solver, and a self-starter who thrives in a startup environment.

The candidate must live in the San Francisco Bay Area or Austin, TX, and have a US citizenship, permanent residency ("green card"), or a current H1-B visa.