

Senior/Principal DFT Engineer

Flex Logix is the leader in eFPGA with working silicon and customers in 40, 28/22, 16/12 and 14/12 nm nodes. We are starting now on 7/6nm EFLX eFPGA. Our eFPGA business did \$16M in revenue last year and is cash-flow positive with significant growth expected in 2021.

Flex Logix also has developed an industry-leading inference architecture with superior inference throughput/\$. Our InferX X1 chip and boards are moving into production.

We recently completed a \$55M funding round to accelerate growth of our eFPGA and Inference businesses

RESPONSIBILITIES

DFT engineer will work closely with the DFT architect, physical design team, embedded FPGA technologists, and logic designers to implement appropriate DFT features, structures, and methodologies for our inference products. You will be required to script flows, debug designs and ensure that appropriate coverage is achieved for the designs.

Primary Responsibilities:

- Develop DFT flows for high-quality production
- Implement and verify DFT solutions
- Oversee execution from contractors
- Interface with DFT EDA vendors
- Interface with IP providers for DFT requirements and results
- Collaborate with operations team to ensure successful and timely production of silicon
- Clearly communicate and document methods and scripts
- Clearly communicate status throughout the project

EXPERIENCE AND SKILL REQUIRED

- Experience working on DFT for high-quality products that reached high-volume production
- Deep understanding of how leading DFT industry tools work
- Recent hands-on experience implementing DFT on a significant SOC
- Recent hands-on experience running gate-level simulations (with SDF) to improve coverage
- BSEE/MSEE with 5 or more years of relevant industry experience
- Must have hands-on experience in scripting languages
- Must have hands-on experience in testbench creation and gate-level simulations with SDF

Preferred experience OR willing to quickly learn:

- Understanding of FPGA architectures and how to best test them
- Running logic synthesis and static timing tools

Must be passionate about being part of an aggressive, venture-backed startup team that is changing chip architecture. Must be entrepreneurial, innovative problem solver and willing to work hard.

Must live in Silicon Valley or Austin area and have US citizenship or permanent residency ("green card"), or holding a current H1-B visa